Digitus

PSE Conformance Test Suite

April 9 2026 7:08 PM

Port Count..... 2
Loop Count..... 1
PSE Tested: **Unspecified Type-1 15.4W**

Sifos® Technologies

802.3at Conformance Report

version 5.3.14
report version 5.3.08

Test Mode: 15.4 Watt
Sifos Interop Index*: 100%
Error Log: None

Chassis ID: 192.168.221.105	PSA-3000 Ports						Low	P/F	High	P/F
TestLoop: 1	1-1	1-2	UNITS	Min	Max	Average	Limit		Limit	
Test: det_v										
Open_Circuit_Det_Voc=	9.85	10.18	V	9.85	10.18	10.02	2.8	Pass	30	Pass
Peak_Det_Vvalid=	8.74	9.05	V	8.74	9.05	8.9	3.8	Pass	10	Pass
Min_Det_Vvalid=	4.82	5.16	V	4.82	5.16	5	2.8	Pass	9	Pass
Det_Volt_Step_dVtest=	3.91	3.88	V	3.88	3.91	3.9	1	Pass	7.2	Pass
Detection_Slew=	0.001	0.001	V/usec	0.001	0.001	0.001	0	Pass	0.1	Pass
Good_Sig_Det_Pulse=	3	3	edges	3	3	3	1	Pass	9	Pass
Backoff_Voltage=	0.5	0.6	V	0.5	0.6	0.6	0	Pass	2.8	Pass
Non_802_Discr_?=	0	0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy=	0	0	****	0	0	0	0	Pass	2	Pass
Test: det_i										
Init_Current_Isc=	0.73	0.72	mA	0.72	0.73	0.73	0	Pass	5	Pass
Det_Current_Isc=	0.7	0.7	mA	0.7	0.7	0.7	0	Pass	5	Pass
Test: det_range										
Rgood_Max=	27	27	Kohm	27	27	27	26	Pass	32	Pass
Rgood_Min=	17	17	Kohm	17	17	17	16	Pass	19	Pass
Rmid_det=	27	27	Kohm	27	27	27	26	Pass	33	Pass
Cgood_Max=	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Cbad_Stat=	0	0	****	0	0	0	0	Pass	0	Pass
4Pair_Pwr_?=	0	0	****	0	0	0	0	Pass	0	Pass
Test: det_time										
Backoff_Time_Tdbo=	3359	3320	msec	3320	3359	3339.5	2000	Pass	16000	Pass
Eff_Backoff_Tdbo_eff=	3359	3320	msec	3320	3359	3339.5	2000	Pass	16000	Pass
Backoff_Type=	0	0	****	0	0	0	0	Pass	0	Pass
Detection_Time_Tdet=	367	359	msec	359	367	363	5	Pass	500	Pass

Enhanced Test Condition

Total_Det_Time=	367	359	msec	359	367	363	5	Pass	1000	Pass
Test: det_rsource										
PSE_Detect_Source=	0	0	****	0	0	0	0	Pass	1	Pass
Output_Impedance_Zout=	0	0	Kohm	0	0	0	45	Pass	2000	Pass
Test: class_v										
Vclass_Max=	19	19.3	V	19	19.3	19.2	15.5	Pass	20.5	Pass
Vclass_Min=	18.3	18.6	V	18.3	18.6	18.5	15.5	Pass	20.5	Pass
Test: class_time										
Class_0_Count=	1	1	****	1	1	1	0	Pass	1	Pass
Class_4_Count=	1	1	****	1	1	1	0	Pass	1	Pass
Class_Time_Tpdc=	14	13.8	msec	13.8	14	13.9	6	Pass	75	Pass
Test: class_err										
Class_lim=	68	70	mA	68	70	69	51	Pass	100	Pass
Pwr_Cl_lim=	0	0	****	0	0	0	0	Pass	1	Pass
Pwr_Cl_55=	0	0	****	0	0	0	0	Pass	1	Pass
Test: pwrup_time										
Power-On_Trise_c0=	88	92	usec	88	92	90	15	Pass	50000	Pass
Power-On_Tpon_c0=	13.7	13.7	msec	13.7	13.7	13.7	0	Pass	400	Pass
Test: pwrup_inrush										
Init_Iinrush=	436.13	435.75	mA	435.75	436.13	435.9	400	Pass	450	Pass
Max_Iinrush_c0=	439	437.5	mA	437.5	439	438.3	400	Pass	450	Pass
Min_Iinrush=	433.38	432.13	mA	432.13	433.38	432.8	400	Pass	450	Pass
Tinrush=	63.9	62.3	msec	62.3	63.9	63.1	50	Pass	75	Pass
Inrush_45m=	48.8	49	V	48.8	49	48.9	44	Pass	57	Pass
Inrush_Voltage=	31.3	31.2	V	31.2	31.3	31.3	30	Pass	57	Pass
Inrush_Strategy_c0=	0	0	****	0	0	0	0	Pass	1	Pass
Test: pwron_v										
Vport_min=	48.3	48.6	V	48.3	48.6	48.5	44	Pass	57	Pass
Vport_max=	49	49.2	V	49	49.2	49.1	44	Pass	57	Pass
Vport_ripple=	11	9	mVpp	9	11	10	0	Pass	500	Pass
Vport_noise=	63	65	mVpp	63	65	64	0	Pass	200	Pass
Vtrans_min=	48	48.4	V	48	48.4	48.2	44	Pass	57	Pass
Vtrans_max=	49.1	49.3	V	49.1	49.3	49.2	44	Pass	57	Pass
Test: pwron_pwrcap										
Pcon_c0=	17.5	17.5	W	17.5	17.5	17.5	14.2	Pass	22.7	Pass

Icon_%_c0=	117.8	118.4	%	117.8	118.4	118.1	100	Pass	9999	Pass
Pcon_c1=	4.5	4.6	W	4.5	4.6	4.6	3.9	Pass	22.7	Pass
Icon_%_c1=	114.6	115.7	%	114.6	115.7	115.2	100	Pass	9999	Pass
Pcon_c2=	8.2	8.2	W	8.2	8.2	8.2	6.8	Pass	22.7	Pass
Icon_%_c2=	118.6	119.1	%	118.6	119.1	118.9	100	Pass	9999	Pass
Pcon_c3=	17.5	17.5	W	17.5	17.5	17.5	14.2	Pass	22.7	Pass
Icon_%_c3=	117.7	118.4	%	117.7	118.4	118.1	100	Pass	9999	Pass
Pcon_c4=	17.5	17.5	W	17.5	17.5	17.5	14.2	Pass	22.7	Pass
Icon_%_c4=	117.7	118.4	%	117.7	118.4	118.1	100	Pass	9999	Pass
Test: pwron_maxi										
Ilim_Peak_1=	445.5	437.5	mA	437.5	445.5	441.5	0	Pass	1750	Pass
Ilim_Min_1=	403.5	401.9	mA	401.9	403.5	402.7	400	Pass	1750	Pass
Tlim_1=	64.1	62.9	msec	62.9	64.1	63.5	50	Pass	75	Pass
Vlim_1=	48	48.4	V	48	48.4	48.2	44	Pass	57	Pass
Ilim_Max_1=	472.3	470	mA	470	472.3	471.2	400	Pass	1750	Pass
Ktran_lo_1=	-1	-1	%	-1	-1	-1	-1	Pass	-1	Pass
Test: pwron_overld										
%Ipeak_1=	125	125	%	125	125	125	100	Pass	125	Pass
Vport_Ipeak_1=	48.1	48.4	V	48.1	48.4	48.3	44	Pass	57	Pass
Vport_5%DC_1=	48.1	48.5	V	48.1	48.5	48.3	44	Pass	57	Pass
Test: mps_dc_valid										
Min_Valid_Time_Tmps=	10	10	msec	10	10	10	1	Pass	60	Pass
Duty_Cycle_tol=	1	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrdn										
Min_Valid_I_hold=	6	6	mA	6	6	6	5	Pass	10	Pass
Time-to-Shutdown_Tmpdo=	321	317	msec	317	321	319	300	Pass	400	Pass
Test: pwrdn_overld										
Icut_1=	381	381	mA	381	381	381	-1	Pass	1750	Pass
Tcut_1=	63.7	62.7	msec	62.7	63.7	63.2	50	Pass	9999	Pass
Isoft_1=	-1	-1	mA	-1	-1	-1	-1	Pass	399	Pass
Tsoft_1=	-1	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Test: pwrdn_time										
Turn-Off_Time_Toff=	38.1	34.5	msec	34.5	38.1	36.3	0	Pass	500	Pass
Output_Cap_Cout=	0.0738	0.0672	uF	0.0672	0.0738	0.07	-1	Pass	0.52	Pass
Output_Load_Rp=	449.4	441.3	Kohm	441.3	449.4	445	45	Pass	50000	Pass

Test: pwrdn_v										
Avg_Idle_Voff=	0.4	0.5	V	0.4	0.5	0.5	0	Pass	2.8	Pass
Error_Delay_Ted=	2765.6	3007.8	msec	2765.6	3007.8	2887	750	Pass	10000	Pass
Peak_Error_Delay_Ved=	1.2	1.2	V	1.2	1.2	1.2	0	Pass	20.5	Pass
Test Port Model Number:	**3202**	**3202**								
Test Port Hardware Version:	**11**	**11**								
Test Port Firmware Version:	**4.19 lc19**	**4.19 lc19**								